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[Optimizing an ANSI C interpreter with superoperators](#) - [►uvic.ca](#) (PDF)

TA Proebsting - Proceedings of the 22nd ACM SIGPLAN-SIGACT symposium on ..., 1995 - portal.acm.org
... implemen- tation of a hybrid translator/interpreter that em ... Compilers typically
translate source code into ... chine's instruction set, but an interpreter writer ...

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[BOOK] [Efficient implementation of the smalltalk-80 system](#)

LP Deutsch, AM Schiffman - 1984 - ACM New York, NY, USA

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[... and method of minimizing performance degradation of an instruction set translator due to self- ...](#)

JS Langford - US Patent 5,875,318, 1999 - Google Patents

... such an extent that it becomes as slow as, or slower than, an instruction set
interpreter. ... The instruction set translator will first translate blocks of ...

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[PA-RISC to IA-64: Transparent Execution, No Recompilation](#) - [►hp.com](#) (PDF)

C Zheng, C Thompson - 2000 - doi.ieeeecs.org

... This combination also provides instruction set architecture (ISA) emulation ... executed
code into native IA-64 instructions. ... make it easy to translate PA-RISC ...

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[Method for providing a pipeline interpreter for a variable length instruction set](#)

JS Yates, SC Root - US Patent 5,802,373, 1998 - freepatentsonline.com

... system 34 will, in general, translate nearly all ... instructions provided from the
background translator 34, and ... program written for a non-native instruction set. ...

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[UQBT: Adaptable Binary Translation at Low Cost](#)

C Cluientes, M Van Emmerik - COMPUTER, 2000 - doi.ieeecomputersociety.org

... than writing a handcrafted binary translator from scratch. ... translations to bytecodes
(the instruction set of the ... uqbtsb and uqbtpb, to translate from Sparc and ...

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[Guest Editors' Introduction: Welcome to the Opportunities of Binary Translation](#)

ER Altman, D Kaeli, Y Sheffer - COMPUTER, 2000 - doi.ieeecomputersociety.org

... the software methods that let you translate at runtime ... user program to determine
the minimal set of blocks ... function and inserts an iniprof instruction in the ...

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[FX! 32 a profile-directed binary translator](#)

A Chernoff, M Herdeg, R Hookway, C Reeve, N Rubin, ... - Micro, IEEE, 1998 - [ieeexplore.ieee.org](#)
... translator subsequently uses to translate executed x86 ... and stores, the translator
coalesces increments ... expose the alternative instruction set architecture (ISA ...
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Dynamic and Transparent Binary Translation

M Gschwind, ER Altman, S Sathaye, P Ledak, D ... - COMPUTER, 2000 - [doi.ieeecomputersociety.org](#)
... scheduling approach used by the BOA translator makes it ... BOA copies all registers
to a set of backup ... Within a trace, BOA schedules instructions out of order and ...
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The design of a resourceable and retargetable binary translator

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... by us- ingknowledgeofthe source-machine callingconventions and instruction set. ... HRT
L to target-machine instructions, and it ... to identify and translate all code ...
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